



# Product Change Notification

## 104823 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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# Product Change Notification

**Change Notification #:** 104823 - 00  
**Change Title:** D120JCTLSEUROU, PCN 104823-00, FYI, Releases new board assemblies that incorporates 3.3 volt version of QLSLAC, changes micro-controller and changes the front end protection fuses  
**Date of Publication:** February 14, 2005

**Type of Change Notification:**  
FYI - (For Your Information)

**Key Characteristics of the Change:**  
Product Design

## Forecasted Key Milestones:

<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	Feb 21, 2005
<b>Date of First Availability of Post-Conversion Material:</b>	Feb 21, 2005

*The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.*

## Description of Change to the Customer:

QLSLAC manufacturer has discontinued production of the 5 volt device. This event has required a board redesign to accept the new 3.3 volt QLSLAC. New board assembly incorporates a 3.3 volt version of QLSLAC in place of 5 volt version and changes the micro-controller to provide timing required for the 3.3 volt QLSLAC. Additionally a change of the front end protection fuses on the Daughter board from 1¼ Amp to ½ Amp has been implemented.

## Customer Impact of Change and Recommended Action:

In order to download the correct parameter files for the new D120JCTLSUEU2 board the following System software release needs to be used.

### Windows:

System Release -Intel® Dialogic® System Release 5.1.1 Feature Pack 1 Service Update build 46 or later must be used. This software is located at <http://developer.intel.com/design/telecom/support/> Use the link stating: Download Intel® Dialogic® System Release 5.1.1 Feature Pack 1 Service Update xx

Linux:

Linux users need to contact Sachin Dev at [sachin.dev@intel.com](mailto:sachin.dev@intel.com) or your sales representative to obtain parameter file information.

This change is a design enhancement and has no impact on units in the field.

This change has been thoroughly evaluated to ensure that it does not have any negative functional implications to our customer.

Customers should modify their databases in order to place orders against the new order codes. Note a new Product Code and MM number has been assigned. See details below.

**Products Affected / Intel Ordering Codes:**

**Board Products Table**

Affected Product Code	Pre-Change MM#	Post Change Product Code	Post-Change MM#
D120JCTLSEUROU	850181	D120JCTLSUEU2	859747

**Reference Documents / Attachments:**

**Document:**

**Location #:**

Intel® Dialogic® System

Release 5.1.1 Feature Pack 1 <http://developer.intel.com/design/telecom/support/>

Service Update 46 or later

**PCN Revision History:**

**Date of Revision:**

February 14, 2005

**Revision Number:**

00

**Reason:**

Originally Published PCN