



Product Change Notification

108648 - 00

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Product Change Notification

Change Notification #: 108648 - 00
Change Title: Boxed Dual-Core Intel® Xeon® 5100 Series (Woodcrest), PCN 108648-00, Product Design, B2 to G0 Stepping Change
Date of Publication: July 15, 2008

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	Aug 15, 2008
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Description of Change to the Customer:

The Boxed Dual-Core Intel® Xeon® processors 5100 Series Processors (Woodcrest) will convert from B-2 to G-0. Woodcrest will undergo the following change as it converts to G-0:

- Increased Energy Efficiency
- Certain SKUs will receive some amount of C1E power reduction on G-step
- SKUs already at the lowest bus to core frequency ratio or lowest voltage operating point cannot drop to the lower power state required for Extended HALT
- Enhanced Virtualization Technology
- APIC Task Priority Register (TPR) Virtualization
- NMI-window exiting
- Advanced VM-exit information for INS & OUTS
- Woodcrest CPUID will change from 06F6 to 06FB
- New SSPEC and MM numbers for the converting products

Customer Impact of Change and Recommended Action:

Intel has developed a technical solution to allow mixed stepping configurations of B-step and G-step Woodcrest on Bensley platforms. This solution requires appropriate microcode updates to be loaded; current microcode updates for Woodcrest B-2 stepping will need to be replaced. Solution requires BIOS change; BIOS will detect mixed stepping configuration and set a bit in a Model Specific Register (MSR).

Customers creating their own BIOS should adhere to the guidelines provided in the "Dual-Core Intel(R) Xeon(R) Processor 5100 Series [Woodcrest] Mixed Steppings Support Guide, May 2007" that can be found on IBL

- IBL Path: IBL Home > Information Desk > Processors > Server/Workstation > Dual Core Intel® Xeon® Processor 5100 Series > Technical

Once customers implement the BIOS update, they will be able to support same stepping and mixed stepping configurations. Customers should be ready to receive a combination of both B-2 stepping material and G-0 stepping material by the "Date Customer Must be Ready to Receive Post-Conversion Material" above.

Products Affected / Intel Ordering Codes:

Processor #	Frequency	Stepping	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Conversion Product Code	Post Conversion S-Spec	Post Conversion MM#
5130	2.00G 4M 1333	B-2	BX805565130A	S LABP	890802	BX805565130P	SLAGC	892166
5130	2.00G 4M 1333	B-2	BX805565130P	S LABP	890803	BX805565130A	SLAGC	892168

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

July 15, 2008

Revision Number:

00

Reason:

Originally Published PCN