



Product Change Notification

108395 - 02

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 108395 - 02
Change Title: Quad-Core Intel® Xeon® Processor X5482, X5460, X5450, X5472, E5450, E5472, E5440, E5462, E5430, E5420, E5410, E5405, L5420, & L5410, (Harpertown), Dual-Core Intel® Xeon® Processor X5272, X5260, E5205, & L5240 (Wolfdale-DP), PCN 108395-02, Product Design, C-0 to E-0 Conversion, Reason for Revision: Adjust Milestone Dates
Date of Publication: May 14, 2008

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date of Samples Availability:	Jul 03, 2008
Date Customer Must be Ready to Receive Post-Conversion Material:	Oct 20, 2008
Date of First Availability of Post-Conversion Material:	Oct 06, 2008

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Reason for Revision: Adjust Milestone Dates

The Quad-Core Intel® Xeon® Processor X5482, X5460, X5450, X5472, E5450, E5472, E5440, E5462, E5430, E5420, E5410, E5405, L5420, and L5410 (Harpertown), Dual-Core Intel® Xeon® Processor X5272, X5260, E5205, and L5240 (Wolfdale-DP) Processors will undergo the following changes for the C-0 to E-0 stepping conversion:

- New SSPEC and MM numbers for the converting product
- CPUID will change to: 0x1067A, IDCOD will change to: 0xA005b013
- C-0 package is pin compatible with E-0 package
- X5482 TDP specification changes from 150W to 120W

Otherwise, there are no other changes to Electrical, Mechanical and Thermal processor Specifications. Intel anticipates no changes to customer platforms designed to Intel guidelines.

Customer Impact of Change and Recommended Action:

Minimal re-qualification and/or validation are expected for the stepping conversion due to no feature set changes between the C-0 and E-0 steppings.

The Quad-Core Intel® Xeon® Processor X5482, X5460, X5450, X5472, E5450, E5472, E5440, E5462, E5430, E5420, E5410, E5405, L5420, L5410, &(Harpertown), Dual-Core Intel® Xeon® Processor X5272, X5260, E5205, & L5240 (Wolfdale-DP) will require a BIOS update. Once customers implement the BIOS update, they will be able to accept both C-0 stepping material and E-0 stepping material. Customers should be ready to receive a combination of both C-0 stepping material and E-0 stepping material by the "Date Customer Must be Ready to Receive Post-Conversion Material" above. For customers implementing their own BIOS, it is necessary to incorporate the BIOS changes to support E-0 stepping. Please contact your local Intel representative for further technical questions.

Products Affected / Intel Ordering Codes:

Processor #	Frequency	Pre-Conversion Product Code	Pre-Conversion S-Spec	Pre - Conversion MM#	Post - Conversion Qual Samples QDF#	Post - Conversion Qual Samples MM#	Post -Conversion Product Code	Post - Conversion S-Spec	Post- Conversion MM#
X5482	3.20 GHz	EU80574KL088N	S LANZ	893490	Q FSZ	898162	AT80574KL088NT	S LBBG	898590
X5460	3.16 GHz	EU80574KJ087N	S LANP	893444	Q FTX	898163	AT80574KJ087N	S LBBA	898578
X5450	3.00 GHz	EU80574KJ080NT	S LASB	894390	Q FUB	898168	AT80574KJ080NT	S LBBE	898582
X5472	3.00 GHz	EU80574KL080NT	S LASA	894389	Q FTH	898151	AT80574KL080NT	S LBBB	898579
E5450	3.00 GHz	EU80574KJ080N	S LANQ	893481	Q FUF	898182	AT80574KJ080N	S LBBM	898599
E5472	3.00 GHz	EU80574KL080N	S LANR	893482	Q FTD	898197	AT80574KL080N	S LBBH	898591
E5440	2.83 GHz	EU80574KJ073N	S LANS	893484	Q FTM	898174	AT80574KJ073N	S L BBJ	898592
E5462	2.80 GHz	EU80574KL072N	S LANT	893485	Q FUK	898183	AT80574KL072N	S LBBN	898597
E5430	2.66 GHz	EU80574KJ067N	S LANU	893483	Q FTS	898176	AT80574KJ067N	S LBBK	898594
E5420	2.50 GHz	EU80574KJ060N	S LANV	893486	Q FUP	898181	AT80574KJ060N	S LBBL	898596
E5410	2.33 GHz	EU80574KJ053N	S LANW	893487	Q FUU	898165	AT80574KJ053N	S LBBC	898580
E5405	2.00 GHz	EU80574KJ041N	S LAP2	893492	Q FUJ	898179	AT80574KJ041N	S LBBP	898600
L5420	2.50 GHz	EU80574JJ060N	S LARP	894226	Q FVZ	898186	AT80574JJ060N	S LBBR	898602
L5410	2.33 GHz	EU80574JJ053N	S LAP4	893491	Q FWD	898187	AT80574JJ053N	S LBBS	898603
X5272	3.40 GHz	EU80573KL0966M	S LANH	893470	Q FYJ	898198	AT80573KL0966M	S LBAR	898611
X5260	3.33 GHz	EU80573KJ0936M	S LANJ	893471	Q FYN	898206	AT80573KJ0936M	S LBAS	898612
E5205	1.86 GHz	EU80573KH0366M	S LANG	893468	Q FZB	898207	AT80573KH0366M	S LBAU	898616
L5240	3.00 GHz	EU80573JJ0806M	S LAS3	894690	Q FZP	898212	AT80573JJ0806M	S LBAY	898619

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

Revision Number:

Reason:

April 25, 2008

00

Originally Published PCN

April 28, 2008

01

Clarify CPUID and IDCODE Information

May 14, 2008

02

Adjust Milestone Dates