



# Product Change Notification

## 108415 - 02

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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# Product Change Notification

**Change Notification #:** 108415 - 02  
**Change Title:** Intel® Xeon® Processors E5220, L5238, E5240 (Wolfdale-DP) and L5408 (Harpertown), PCN 108415-02, Product Design, C-0 to E-0 Conversion, Reason for Revision: Clarification on Halide Free Packaging  
**Date of Publication:** July 03, 2008

## Key Characteristics of the Change:

Product Design

## Forecasted Key Milestones:

<b>Date of Samples Availability:</b>	Jul 03, 2008
<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	Oct 20, 2008
<b>Date of First Availability of Post-Conversion Material:</b>	Oct 06, 2008

*The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.*

## Description of Change to the Customer:

### Reason for Revision: Clarification on Halide Free Packaging

The Intel® Xeon® Processors E5220, L5238, and E5240 (Wolfdale-DP) and L5408 (Harpertown) will undergo the following changes for the C-0 to E-0 stepping conversion:

- New SSPEC and MM numbers for the converting product
- CPUID will change to: 0x1067A, IDCODE will change to: 0xA005b013
- C-0 package is pin compatible with the E-0 package
- Package change to Halide free package

There are no changes to Electrical, Mechanical and Thermal processor Specifications. Intel anticipates no changes to customer platforms designed to Intel guidelines.

## Customer Impact of Change and Recommended Action:

Minimal re-qualification and/or validation are expected for the stepping conversion due to no feature set changes between the C-0 and E-0 steppings.

The Intel® Xeon® Processor E5220, L5238, E5240 (Wolfdale-DP) and L5408 (Harpertown) will require a BIOS update. Once customers implement the BIOS update, they will be able to accept both C-0 stepping material and E-0 stepping material. Customers should be ready to receive a combination of both C-0 stepping material and E-0 stepping material by the "Date Customer Must be Ready to Receive Post-Conversion Material" above. For customers implementing their own BIOS, it is necessary to incorporate the BIOS changes to support E-0 stepping. Please contact your local Intel representative for further technical questions.

## Products Affected / Intel Ordering Codes:

Processor #	Frequency	Pre-Conversion Product Code	Pre-Conversion S-Spec	Pre - Conversion MM#	Post - Conversion Qual Samples QDF#	Post - Conversion Qual Samples MM#	Post - Conversion Product Code	Post - Conversion S-Spec	Post-Conversion MM#
L5408	2.13 GHz	EU80574JH046N	S LAP5	893494	Q FWH	898188	AT80574JH046NT	S LBBT	898604
E5220	2.33 GHz	EU80573KJ0536M	S LANF	893467	Q FYX	898208	AT80573QJ0536M	S LBAT	898613
L5238	2.66 GHz	EU80573JJ0676M	S LANM	893473	Q FZX	898215	AT80573JJ0676MT	S LBAZ	898620
E5240	3.00 GHz	EU80573KJ0806M	S LAND	893452	Q FYS	898210	AT80573QJ0806M	S LBAW	898614

## Reference Documents / Attachments:

Document:

Location #:

## PCN Revision History:

Date of Revision:

Revision Number:

Reason:

April 25, 2008

00

Originally Published PCN

April 28, 2008

01

Clarify CPUID and IDCOD  
Information

July 3, 2008

02

Clarification on Halide Free  
Packaging